Automatic Deployment of Streaming Applications on Hybrid Architectures

Roger D. Chamberlain, Mark A. Franklin

Dept. of Computer Science and Engineering,
Washington University in St. Louis
Hybrid Architectures

Graphics Engine

Cell

Chip Multiprocessor

FPGA

DSP
Mapping Application to Resources

Sorting application:
• all on processors
  - place sort kernel on FPGAs
• use both FPGAs and graphics engine