Low Latency Real-Time Computing on Multiprocessor Systems Running Standard Linux

Through proper configuration and adherence to guidelines, a multicore/multiprocessor standard Linux* kernel can run applications requiring low latency and fast interrupt response.

A system configured with SGI* React provides standard Linux processes with real-time response using a standard distribution kernel. No underlying real-time kernel required.

Latency Effects

Interrupt latency—time to process an interrupt and wake a sleeping thread.
- Time until handler invoked
- Time until application thread wakes
- Time until application thread reaches userspace

Jitter—periods of CPU unavailability during thread execution.

Configuring the System

Once you have a distribution free from unexpected latencies installed, configure the system by isolating a subset of CPUs from:
- Processes and threads not associated with the application
- Kernel threads when possible
- Load Balancing Effects
- Interrupts not associated with the application
- Unnecessary timers not associated with the application
- Effects of hyper-threading (turn hyper-threading off)

SGI’s React configuration does this for you. It also sets up cpusets to aid in easily attaching your threads to specific CPUs.

Steps to achieving a low latency Linux OS installation

Install a distribution already suited to real-time work (such as an SGI React supported distro):
- The easiest way
- Vendor supported

Work with the community and a vendor to test for latency issues and get fixes to those issues into their distribution:
- Must check new release for new problems and address those
- Vendor support

Test for latency issues and modify a kernel with your own patches for fixing latencies:
- Fixes likely need to be reapplied with each new release
- Must check new release for new problems and address those
- No vendor support

Configure a set of CPUs for realtime use:

Design and run your application following recommended guidelines

Running Application Threads

Pin to an isolated CPU (using ‘taskset’ or attach to appropriate cpuset).

Lock memory [mlock/mlockall].

Avoid spending time in the kernel.

Use shared memory for IPC.

Avoid locking when possible.

Follow rules for avoiding priority inversion if locking required.

Redirect application interrupt to same CPU running interrupt handling thread.

Best with single thread per isolated CPU, but multi-threading is fine if little or no time is spent in the kernel and priority inversion is strictly avoided.

Create user-level drivers if possible. Facilities such as userspace PCI bus access, SGI ULI (User Level Interrupts), and SGI KBAR (Kernel Barriers) can aid in this.

SGI FRs (Frame Rate Schedulers) allows you to schedule threads periodically using an RTC clock or external interrupts as a sync trigger, again eliminating the need for kernel-level driver programming in certain instances.

With the above strategy, non-preemptive thread latencies can be kept within the 1% of usec range. Jitter will still be in the multi-usec range.

To further reduce jitter during periods of time critical processing, CPU timer interrupts can be switched off for short durations, allowing near-jitter free operation.

The SGI provided SGI shield API allows you to do this from program control.

Highest interrupt response times

4 CPU

<table>
<thead>
<tr>
<th>System</th>
<th>Without React</th>
<th>With React</th>
</tr>
</thead>
<tbody>
<tr>
<td>reload</td>
<td>349.30</td>
<td>11.25</td>
</tr>
<tr>
<td>load</td>
<td>554.95</td>
<td>11.15</td>
</tr>
</tbody>
</table>

64 CPU

<table>
<thead>
<tr>
<th>System</th>
<th>Without React</th>
<th>With React</th>
</tr>
</thead>
<tbody>
<tr>
<td>reload</td>
<td>484.95</td>
<td>10.50</td>
</tr>
<tr>
<td>load</td>
<td>545.35</td>
<td>18.75</td>
</tr>
</tbody>
</table>

SGI poster

Sliced into 12 panels—each at 15” x 11”